

Claims

We claim:

1. An electronic structure comprising:

a substrate having a dielectric layer between a first metal layer and a second metal layer, the second metal layer being disposed above the first metal layer, the first metal layer having a first contact area, the second metal layer having a selected area disposed above the first contact area;

a microvia cavity within the selected area being disposed through the second metal layer and through the dielectric layer and extending to the first contact area of the first metal layer;

a mass of conductive material forming a layer upon the selected area of the second metal layer and being inside the microvia cavity and being in contact with the first contact area of the first metal layer.

2. The structure of claim 1, wherein the mass of conductive material conformally fills the microvia cavity.

3. The structure of claim 1, wherein the mass of conductive material has a planar surface forming a contact pad located parallel to and opposite the first contact area.

1 4. The structure of claim 1, wherein selected area has
2 approximately the shape of a circular disk approximately centered
3 around the first contact area.

1 5. The structure of claim 4, wherein the second metal layer
2 within the selected area contains a flat metal ring that is
3 approximately centered around the microvia cavity.

1 6. The structure of claim 1, wherein the second metal layer
2 within the selected area contains a flat metal ring that is
3 approximately centered around the first contact area.

1 7. The structure of claim 1, wherein the second metal layer
2 contains a flat copper ring around the microvia cavity.

1 8. The structure of claim 1, wherein the microvia cavity
2 includes a truncated cone-shaped hole in the dielectric layer.

1 9. The structure of claim 1, wherein the mass of conductive
2 material comprises at least one of a solder paste, a reflowable
3 solder, a conductive paste, and a conductive adhesive.

1 10. A method for forming contact pads on a substrate, the method
2 comprising:

3 providing a substrate including a first metal surface and an
4 external metal foil layer and a layer of dielectric material
5 disposed between the first metal surface and the external metal
6 foil layer;

7 perforating the external metal foil layer and the dielectric
8 layer to expose a portion of the first metal surface;

9 selectively depositing a conductive material upon the
10 exposed portion of the first metal surface and upon a peripheral
11 area of the external metal foil layer around the exposed portion
12 of the first metal surface;

13 etching the external metal foil layer using the selectively
14 deposited conductive material as an etch mask.

15 11. The method of claim 10 wherein selectively depositing the
16 conductive material includes providing a patterned strippable
17 mask having a screen hole upon a selected area of the external
18 metal layer and screening a conductive paste through the screen
19 hole.

20 12. The method of claim 11 wherein providing a patterned
21 strippable mask having a screen hole includes depositing a
22 strippable mask layer upon the external metal foil layer and

4 removing a selected portion of the strippable mask layer from the
5 selected area to form the screen hole.

1 13. The method of claim 12 wherein removing selected portions of
2 the strippable mask layer to form the screen holes includes laser
3 ablating the selected area.

1 14. The method of claim 10 wherein perforating the external
2 metal foil layer and perforating the dielectric layer includes:
3 at least one of laser ablating the first metal foil layer
4 and laser ablating the dielectric layer.

1 15. The method of claim 10 wherein etching the external metal
2 foil layer further comprises etching an area of the external
3 metal foil layer surrounding the selectively deposited conductive
4 material to produce a planar metal ring formed in the external
5 metal foil layer.

1 16. The method of claim 10 wherein selectively depositing the
2 conductive material forms a contact pad.

1 17. The method of claim 10 wherein etching the external metal
2 foil layer includes removing a surrounding area of the external
3 metal foil layer, where the surrounding area is a portion of the

4 external metal foil layer that is not covered by the selectively
5 deposited conductive material.

1 18. The method of claim 10 wherein further comprising:
2 providing a patterned layer of strippable mask to facilitate
3 the selectively depositing of the conductive material.

1 19. The method of claim 10 wherein the a selected area of the
2 external metal foil layer has a diameter of at least about 2 mils
3 up to about 6 mils.

1 20. An assembly comprising:

2 a semiconductor chip having a circuit;

3 a substrate having a dielectric layer between a first metal
4 layer and a second metal layer, the second metal layer being
5 disposed above the first metal layer, the first metal layer
6 having a first contact area, the second metal layer having a
7 selected area disposed above the first contact area;

8 a microvia cavity within the selected area being
9 disposed through the second metal layer and through the
10 dielectric layer and extending to the first contact area of the
11 first metal layer;

12 a mass of conductive material forming a layer upon the
13 selected area of the second metal layer and being inside the
14 microvia cavity and being in contact with the first contact area
15 of the first metal layer;

16 wherein the circuit is electrically connected to the mass of
17 conductive material.